# Rec'd PTO 18 DEC 2001 09/936 041

PATENT APPLICATION

110553

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE.

In re the Application of

Ichio YUDASAKA et al.

Application No.: 09/936,041

Filed: September 7, 2001 Docket No.:

For: METHOD OF MANUFACTURING A THIN-FILM TRANSISTOR

## SUPPLEMENTAL PRELIMINARY AMENDMENT

Director of the U.S. Patent and Trademark Office

Washington, D.C. 20231

Sir:

As a supplement to the preliminary amendment filed on <u>September 7, 2001</u>, and prior to initial examination, please further amend the above-identified application as follows:

## IN THE ABSTRACT:

Please replace the Abstract filed with the substitute Abstract attached hereto.

## IN THE SPECIFICATION:

Please replace the specification with the substitute specification attached hereto.

### IN THE CLAIMS:

Please replace claims 1-13 as follows:

(Amended) A method of manufacturing a thin-film transistor, comprising:

forming a channel region facing a gate electrode through a gate insulating

